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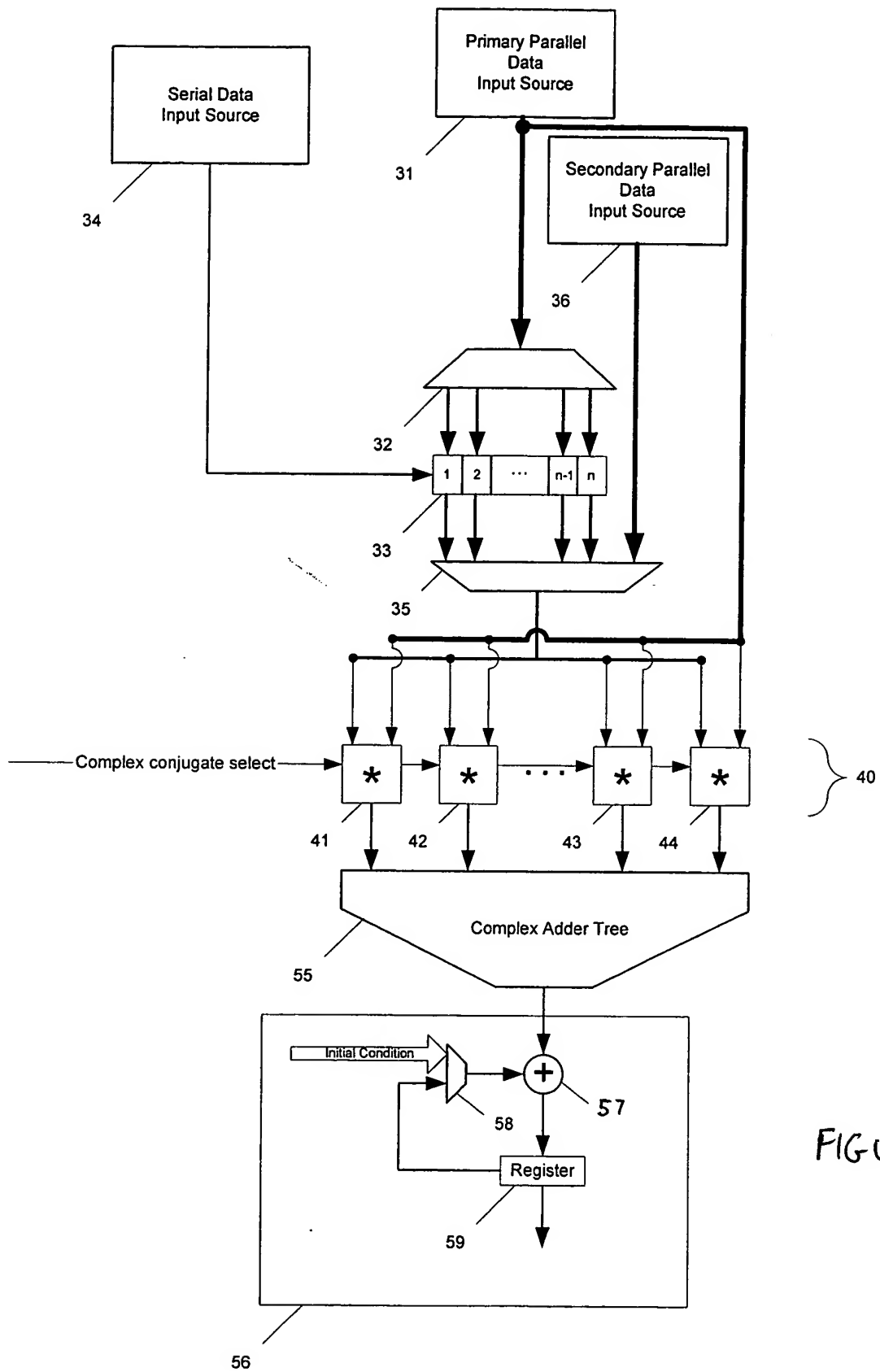
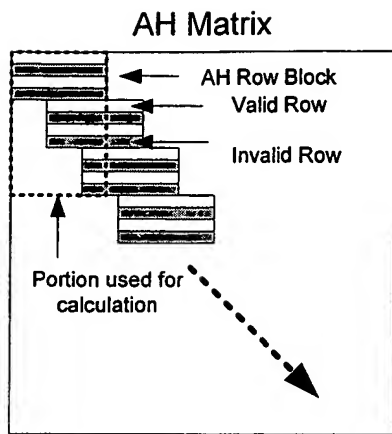
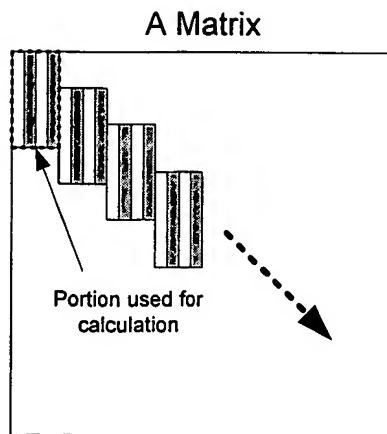


FIGURE 1



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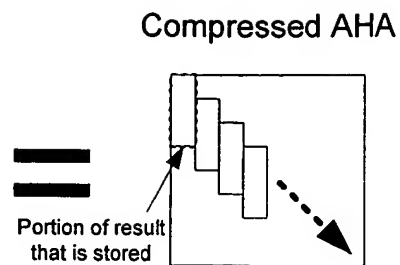
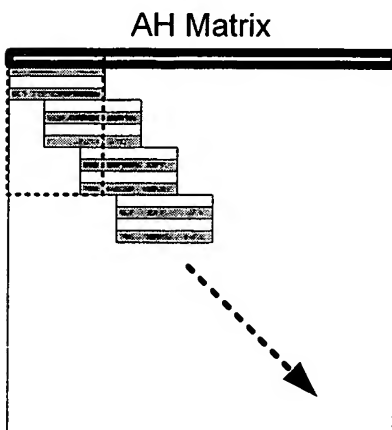
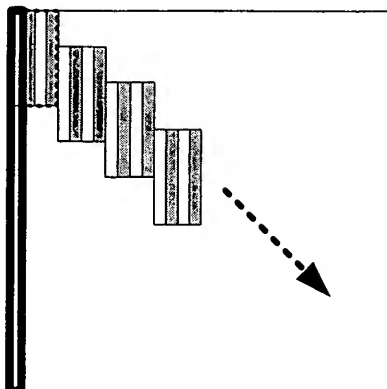


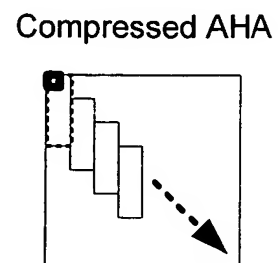
FIG 2A



*



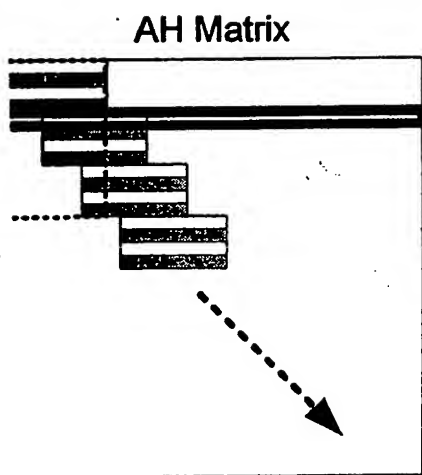
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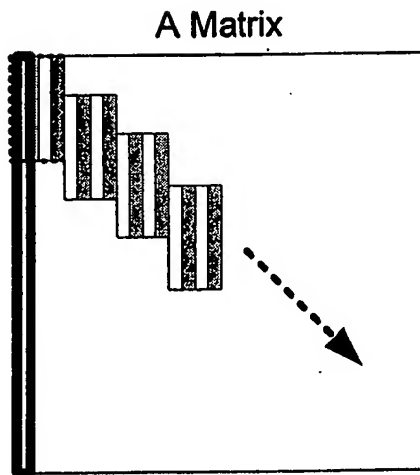
AH Matrix

A Matrix

FIG 2B



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Compressed AHA

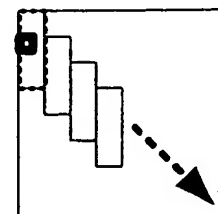


FIG 2C

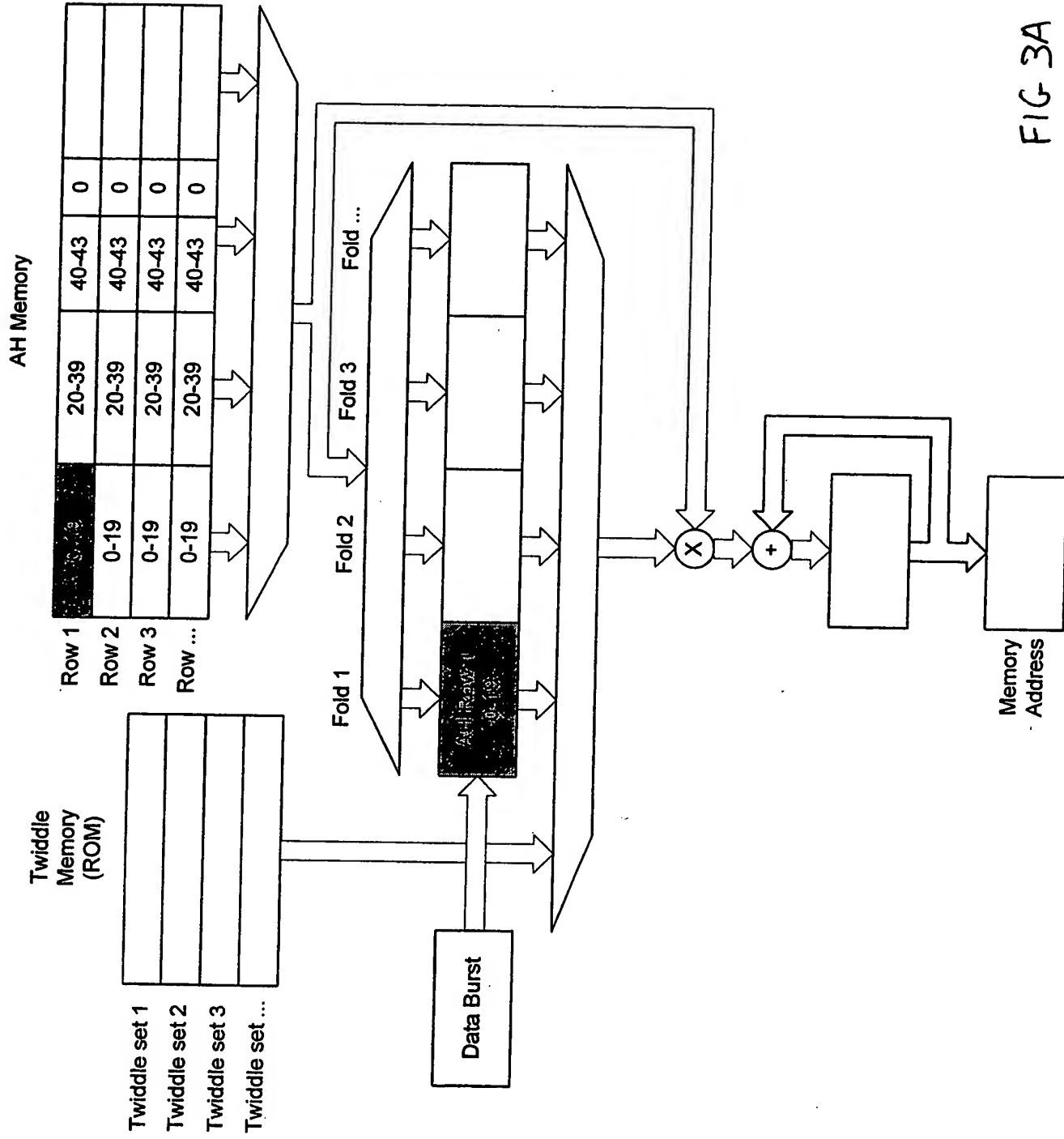


FIG 3A

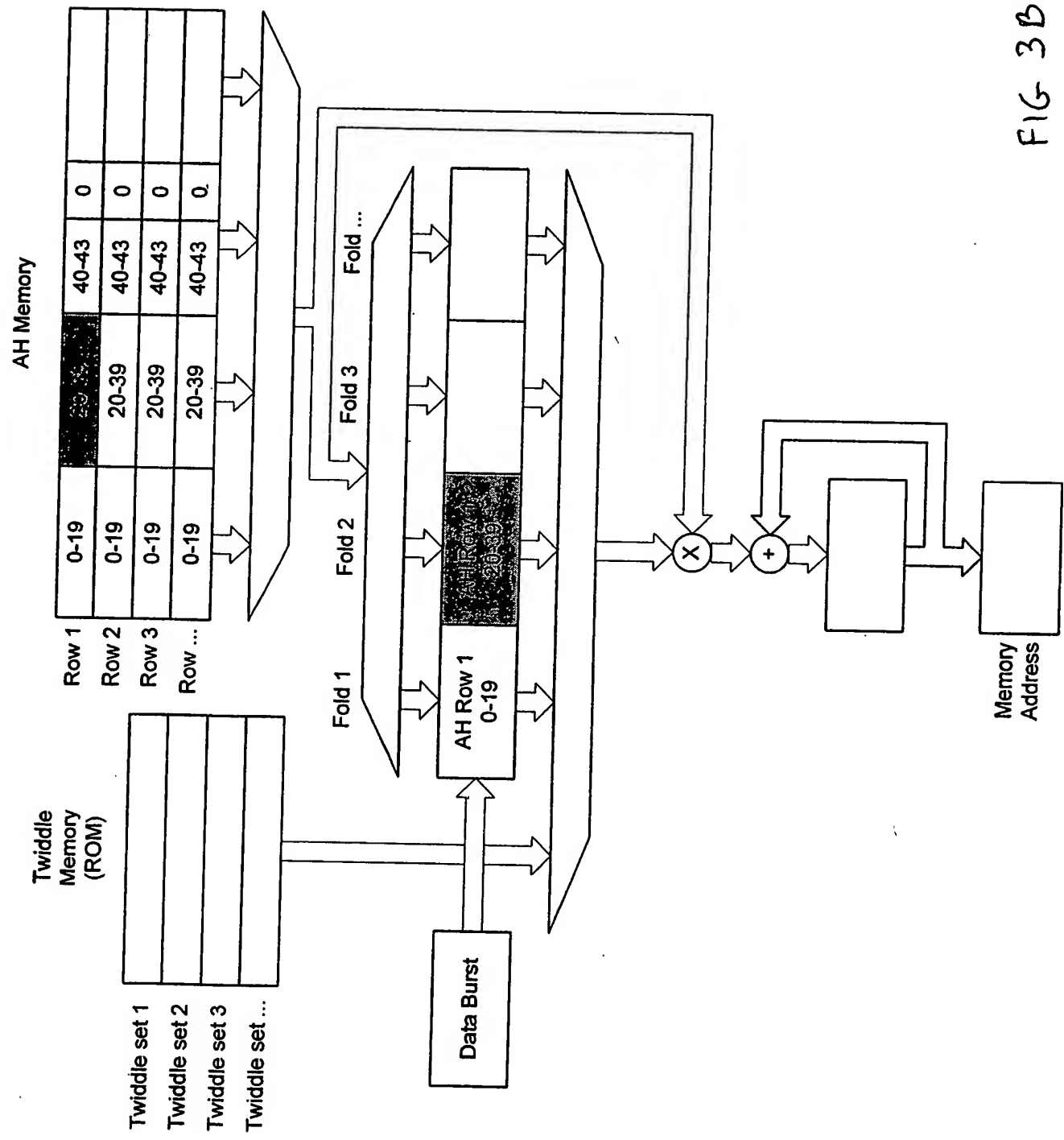


FIG 3B

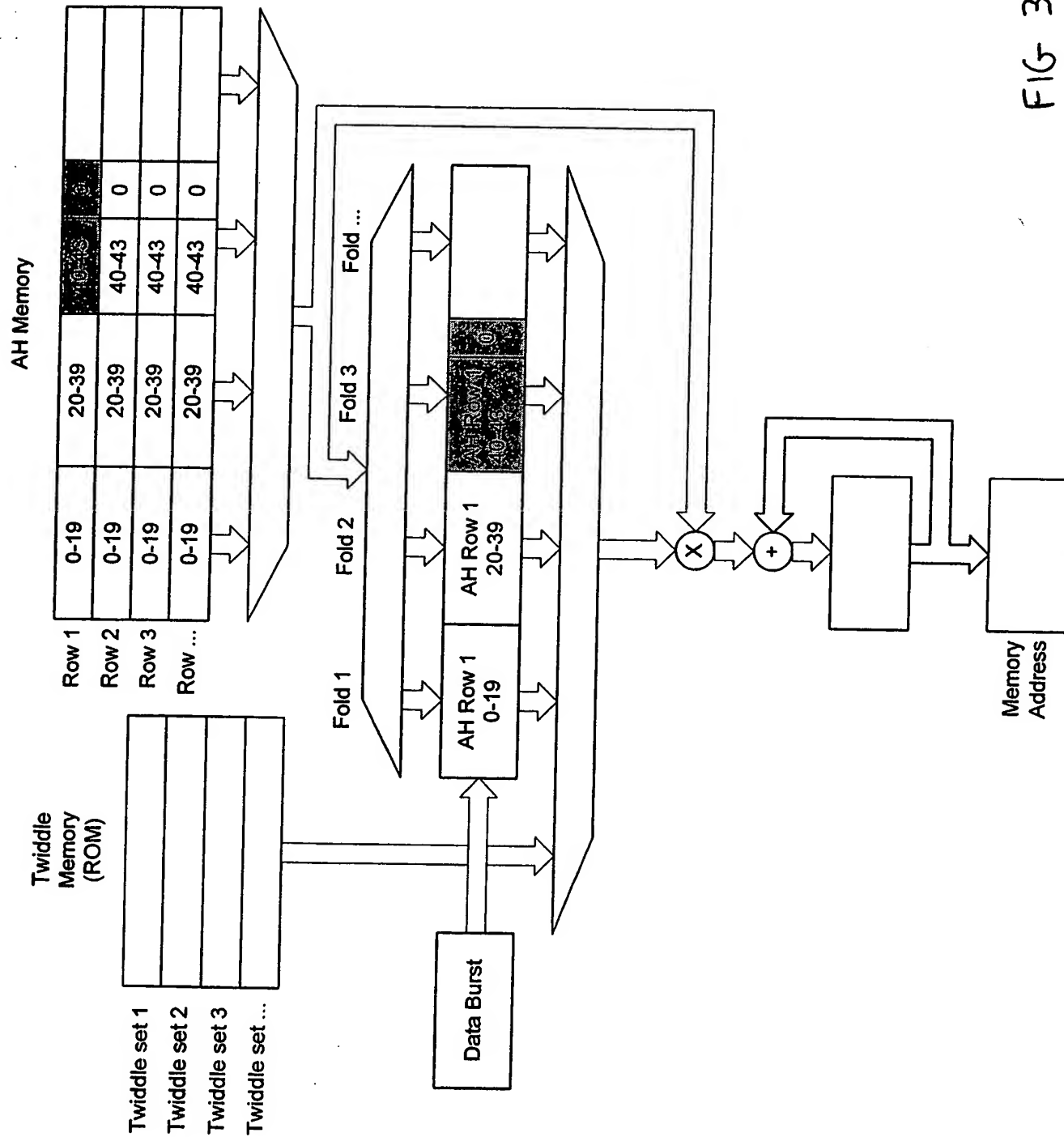


FIG. 3C

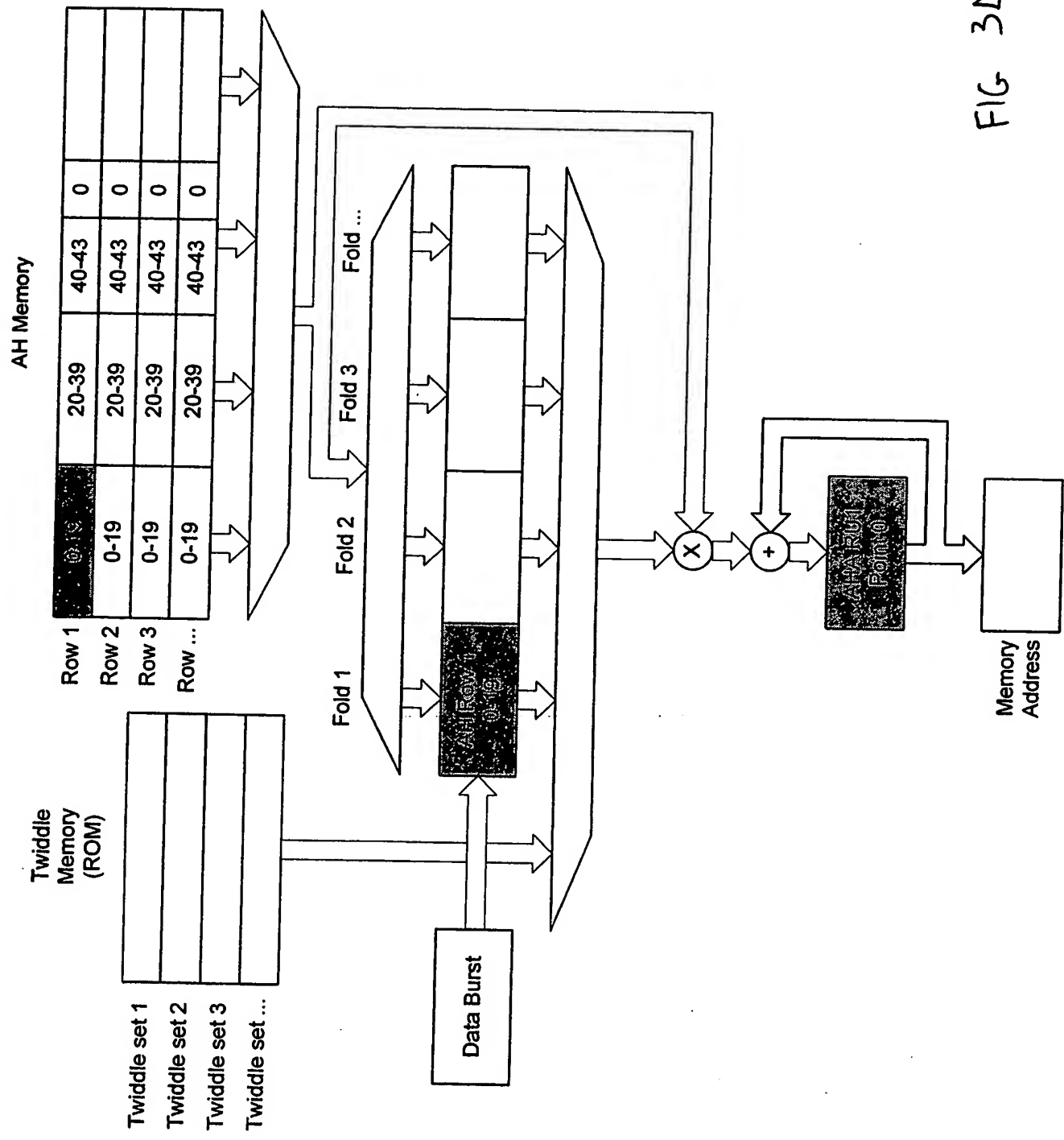


FIG 3D

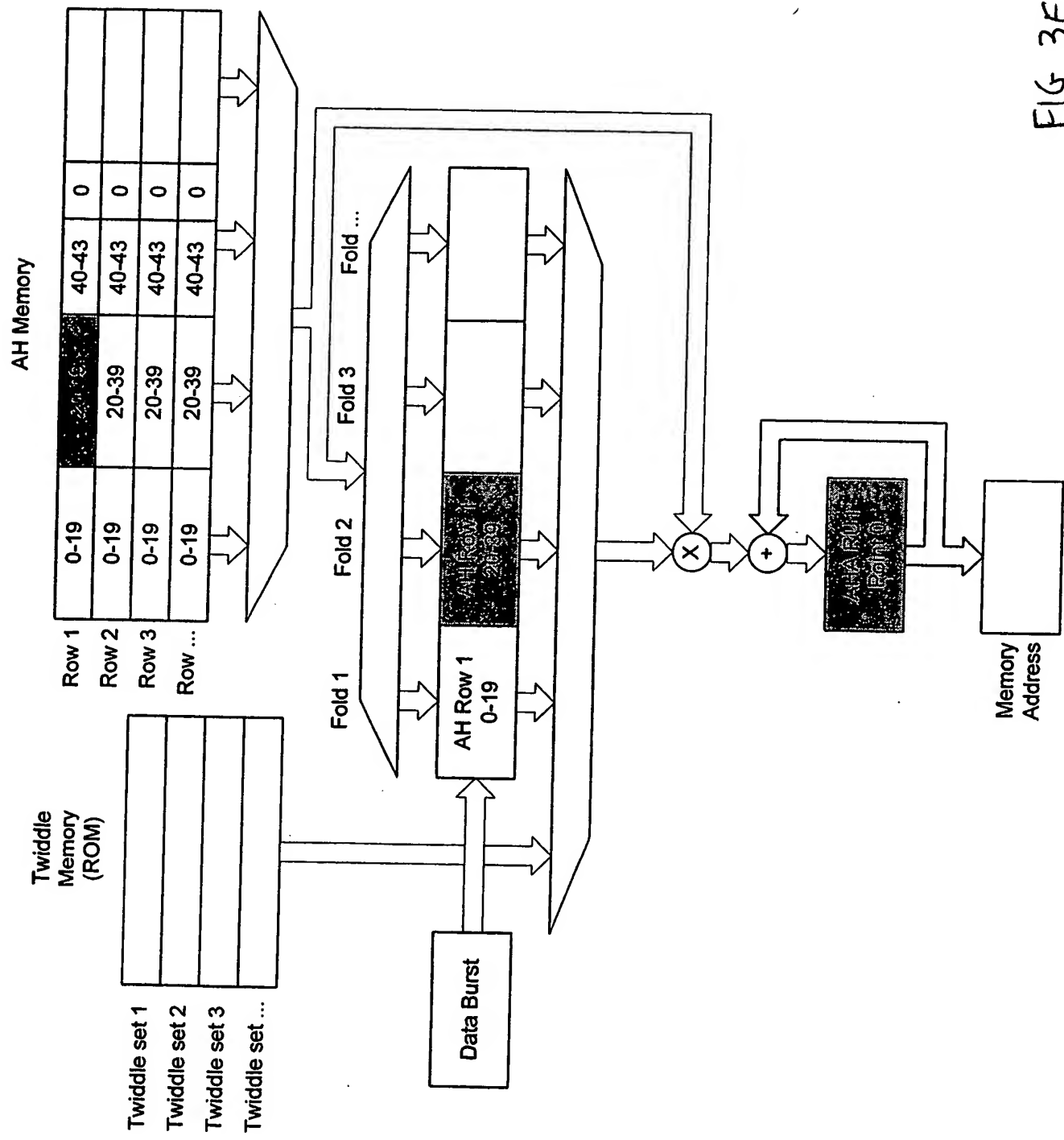


FIG 3E

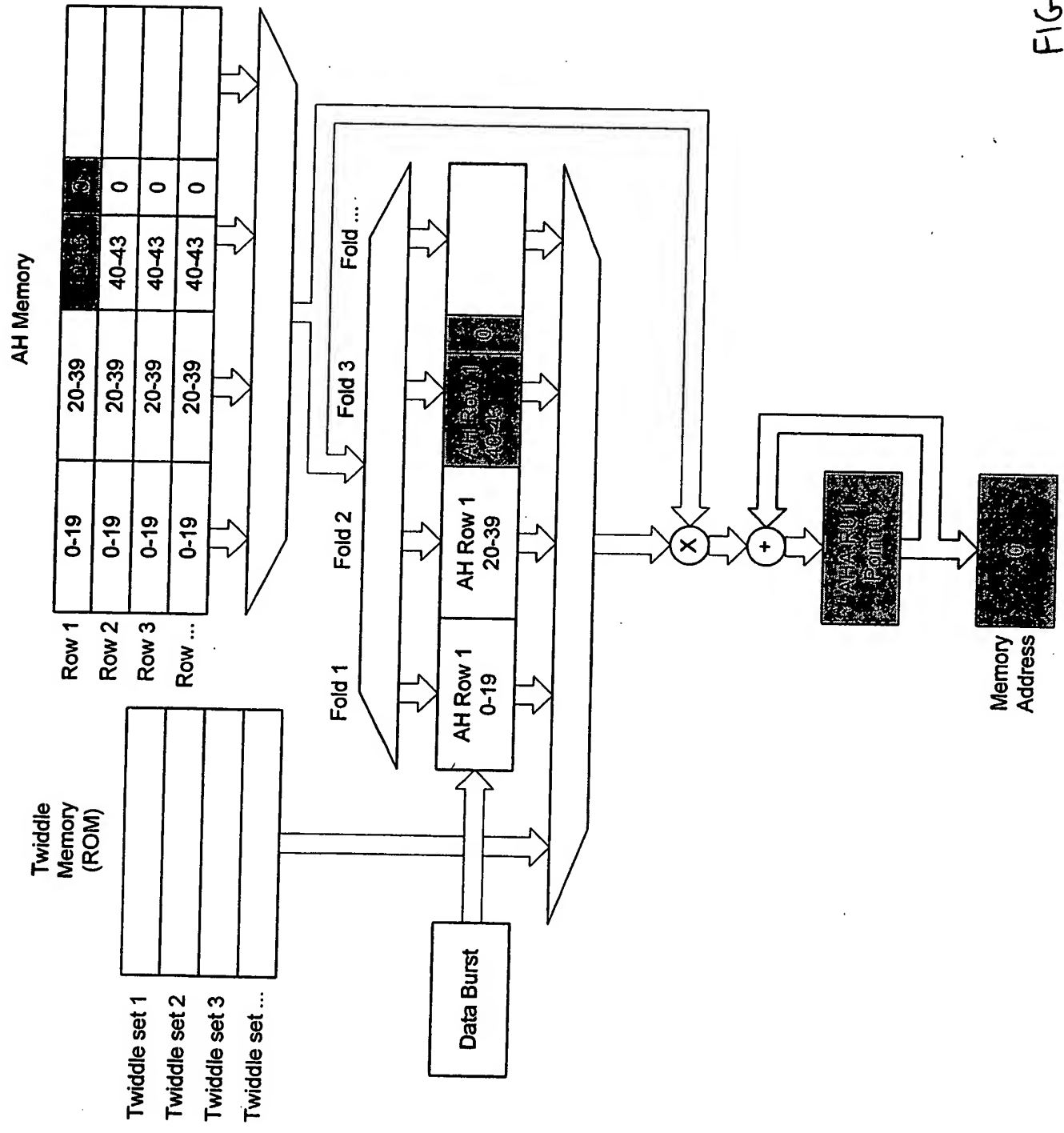


FIG 3F

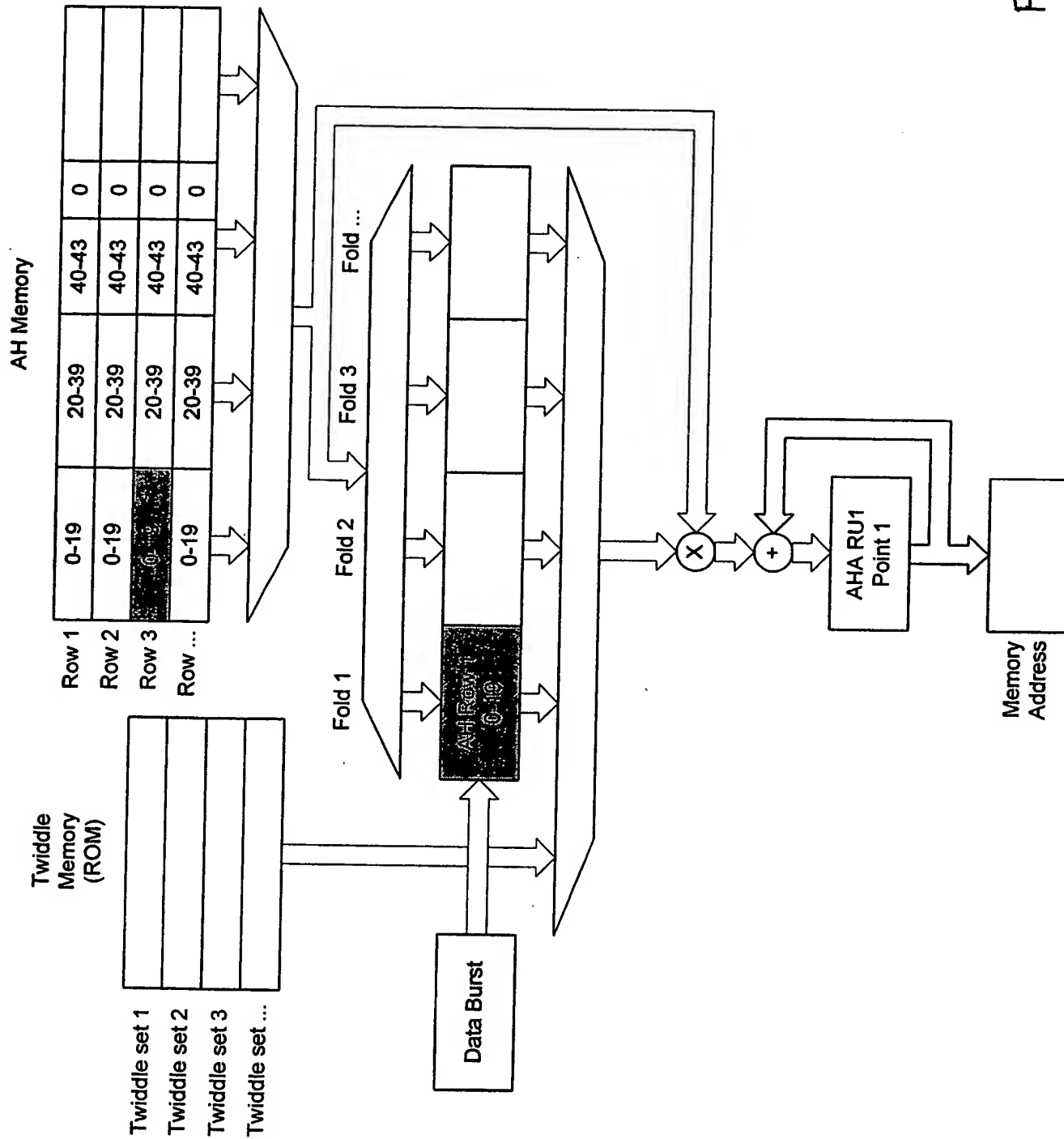
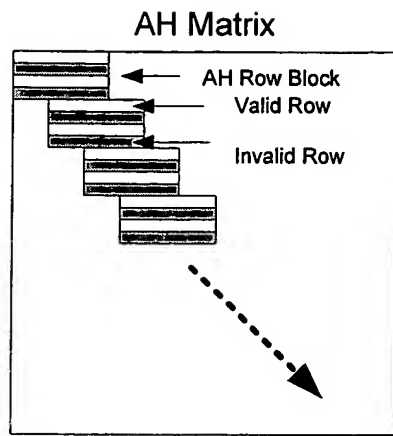


FIG. 3G



*

r

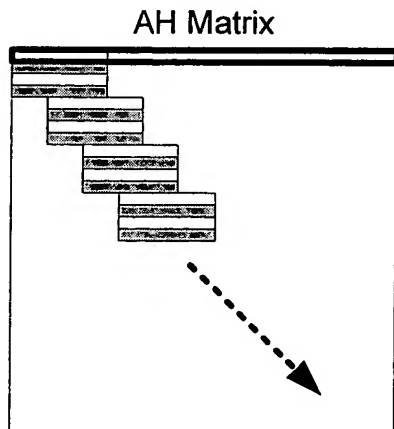


=

AHr



FIG 4A



*

r



=

AHr

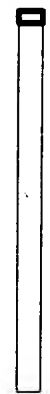
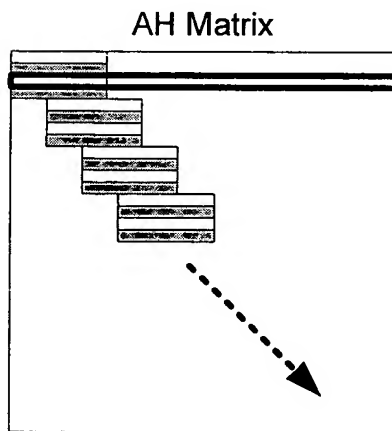


FIG 4B



*

r

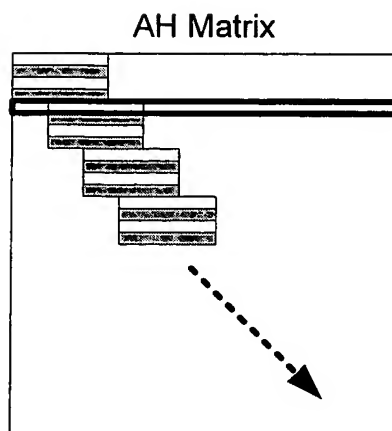


=

AHr



FIG 4C



*

r



=

AHr



FIG 4D

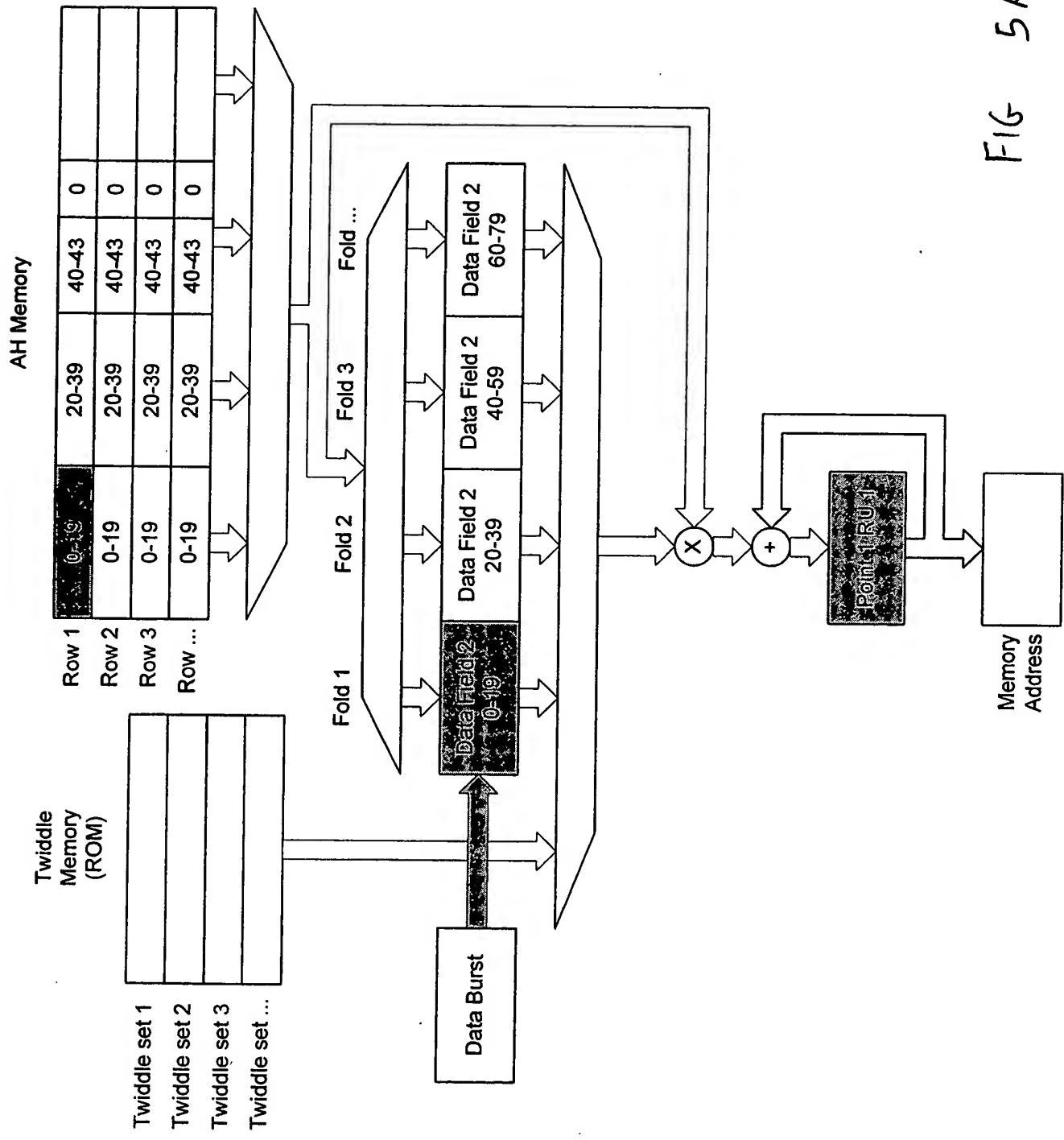


FIG 5A

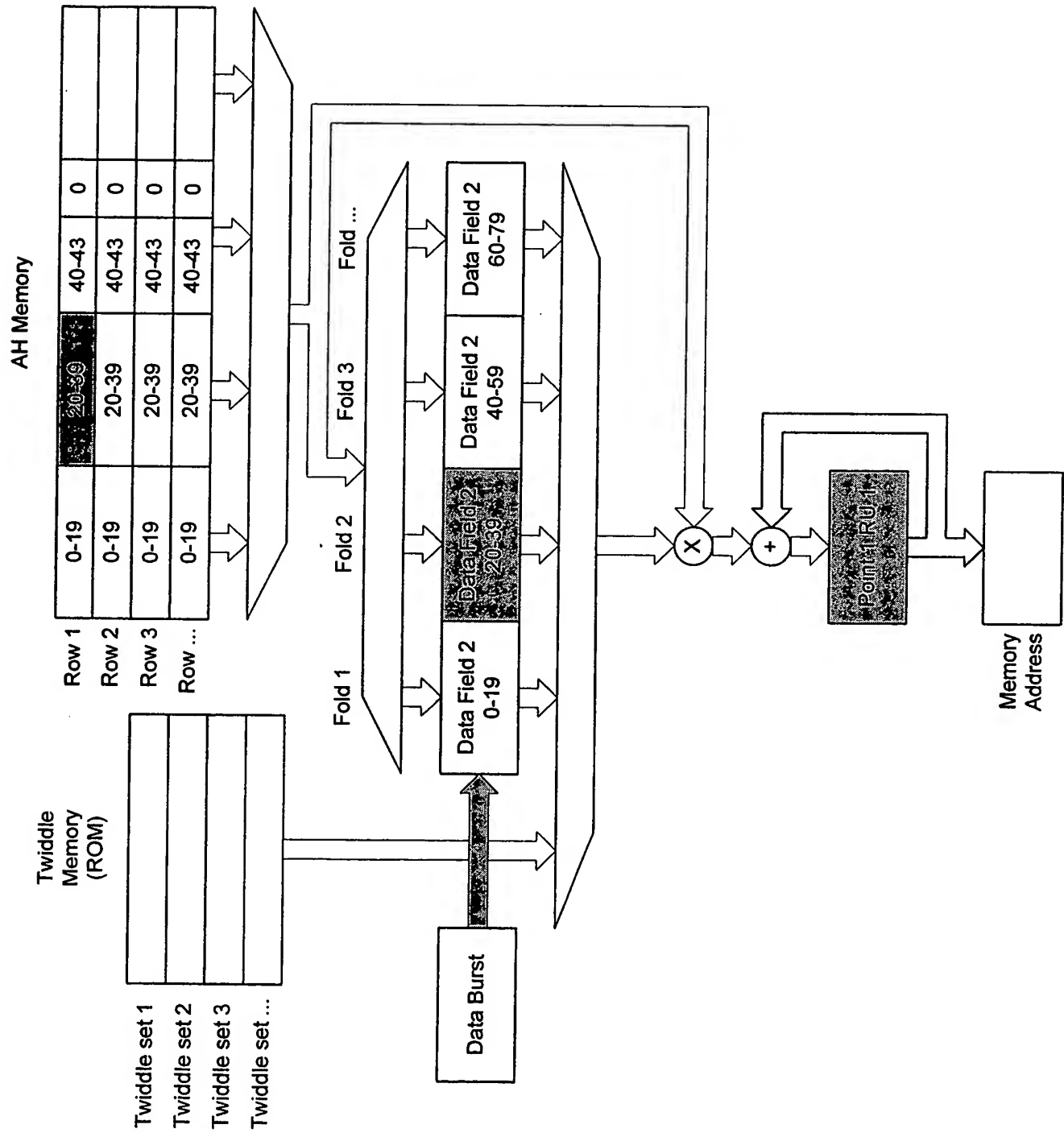


FIG. 5B

AH Memory

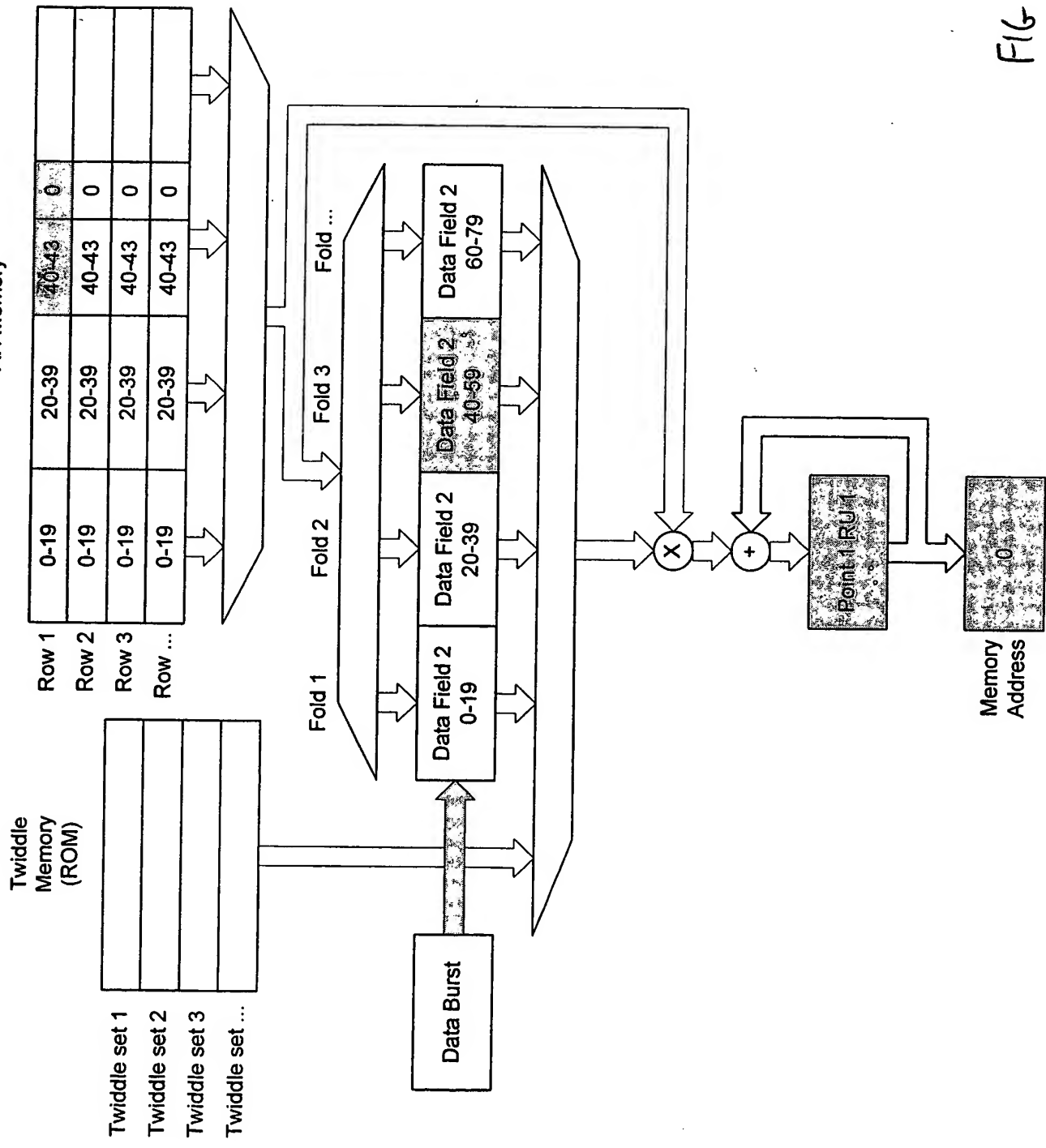


FIG 5C

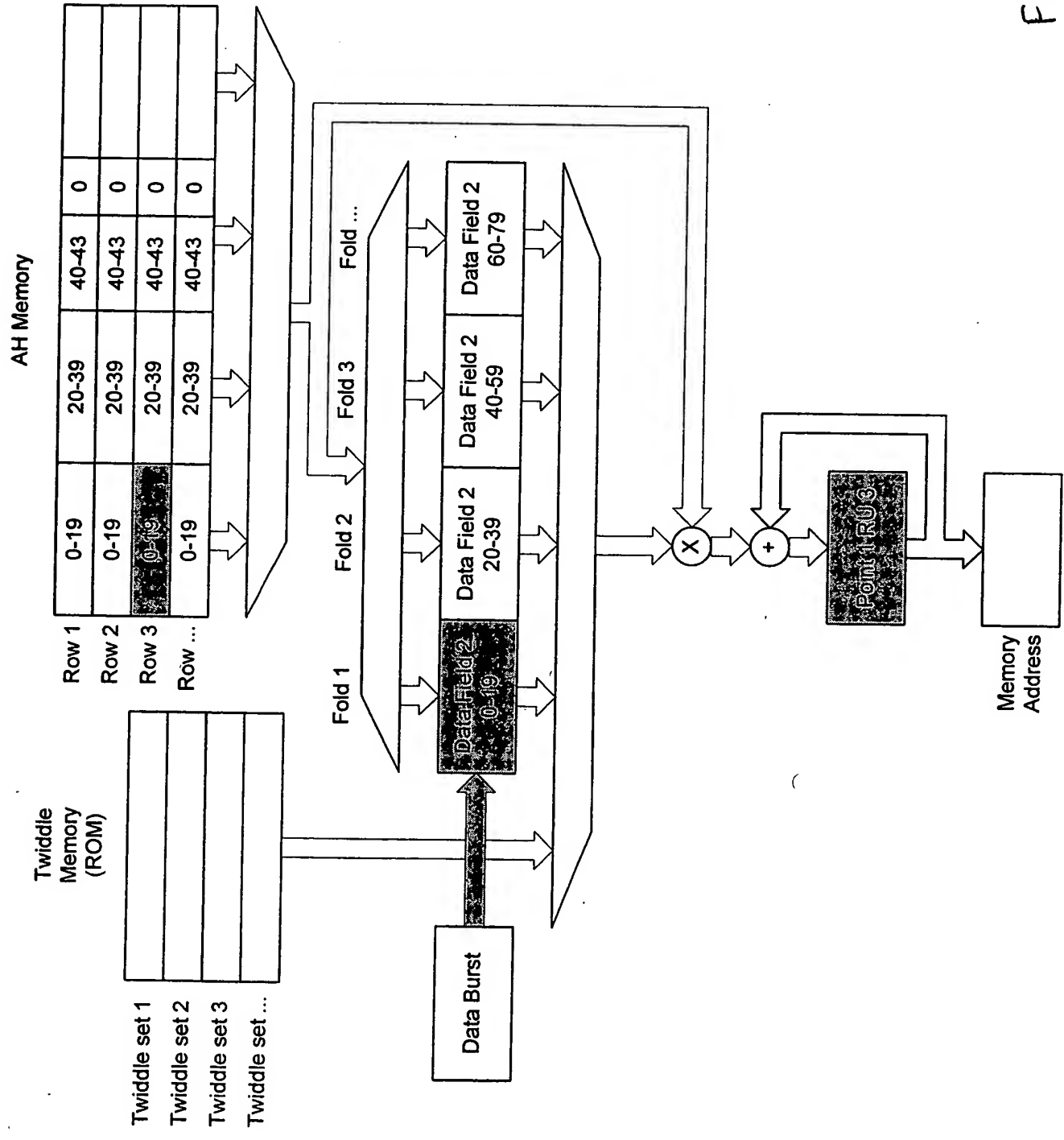


FIG 5A

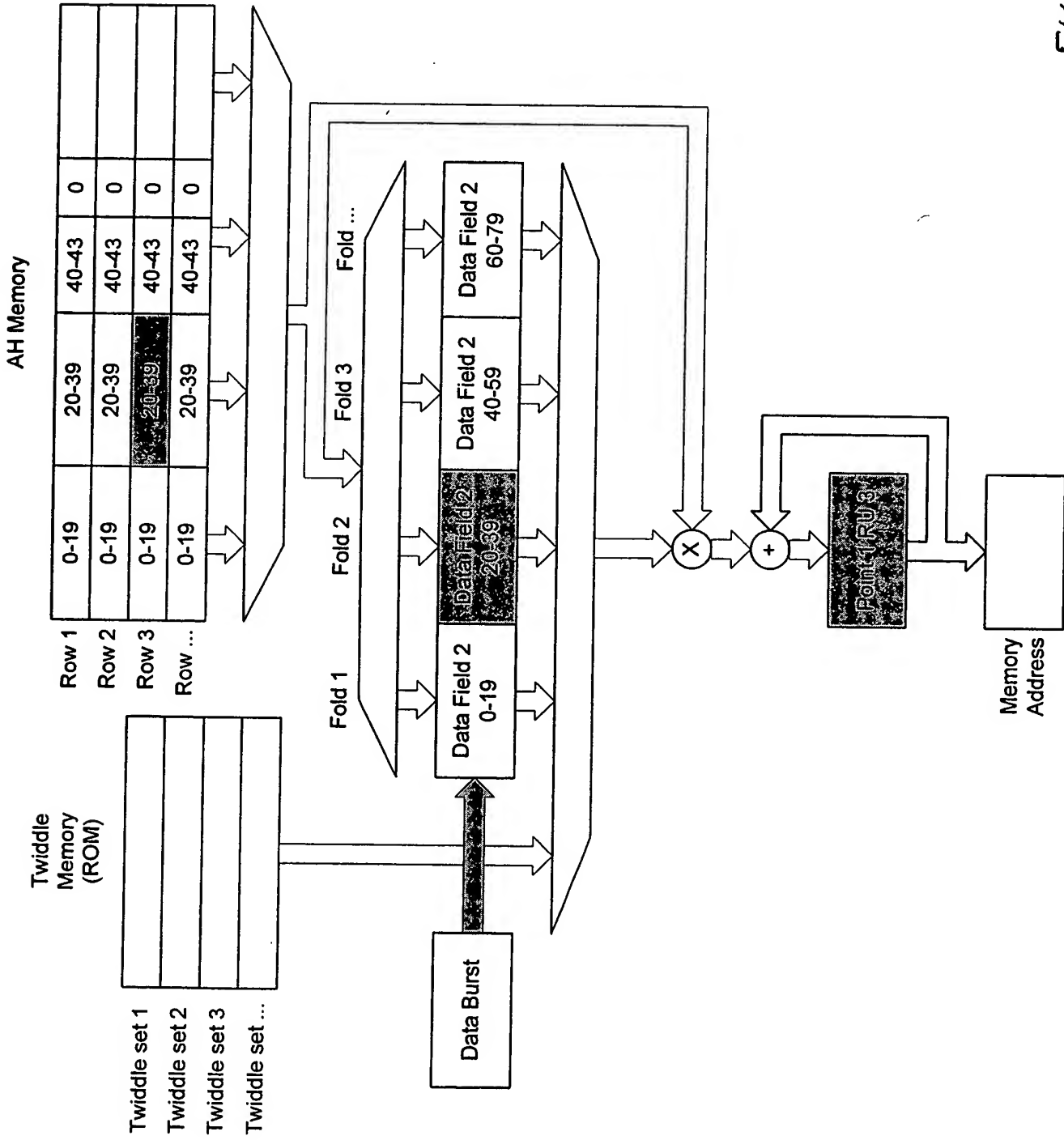


FIG. 5E

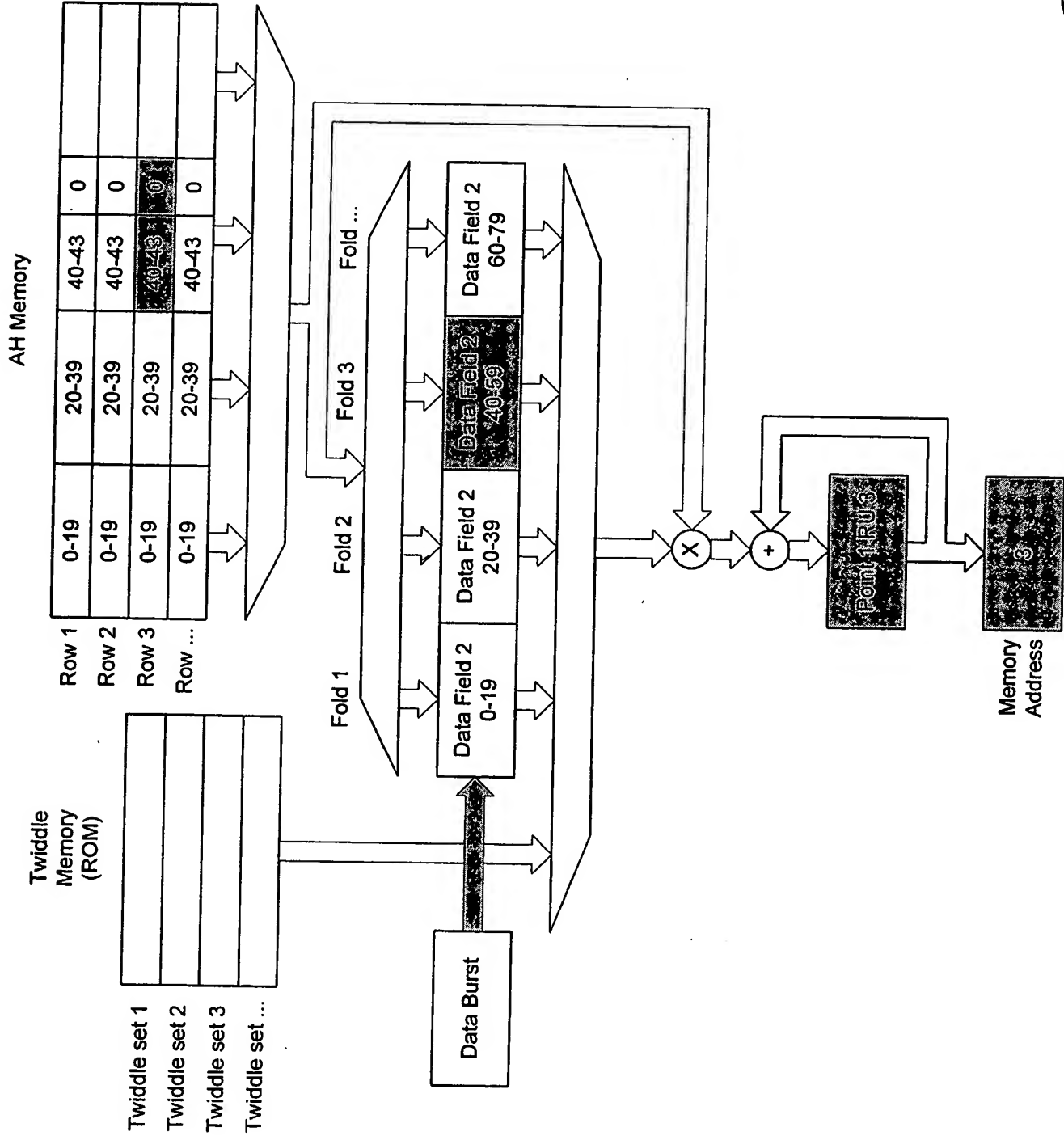


FIG 5F

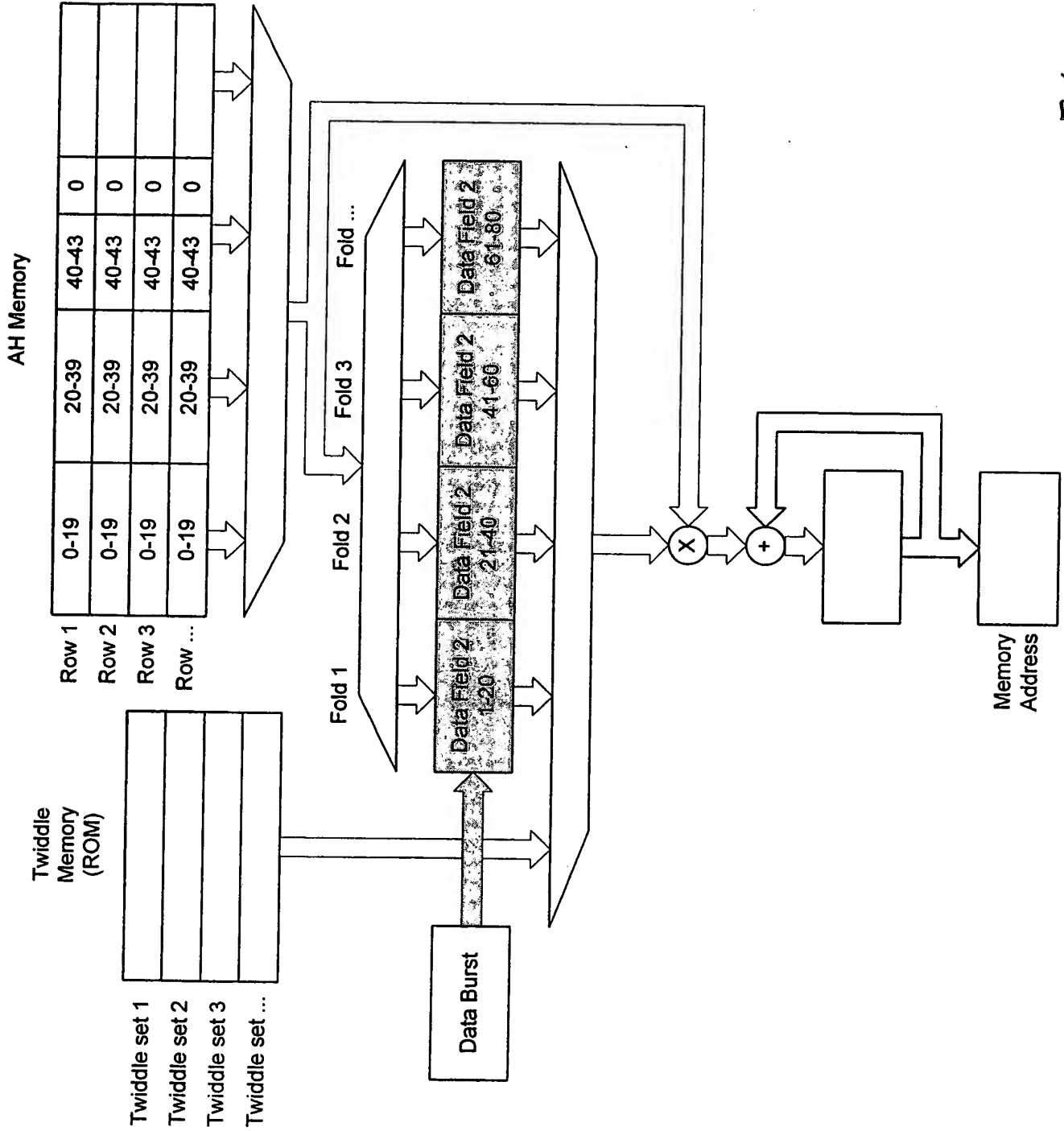


FIG 5G

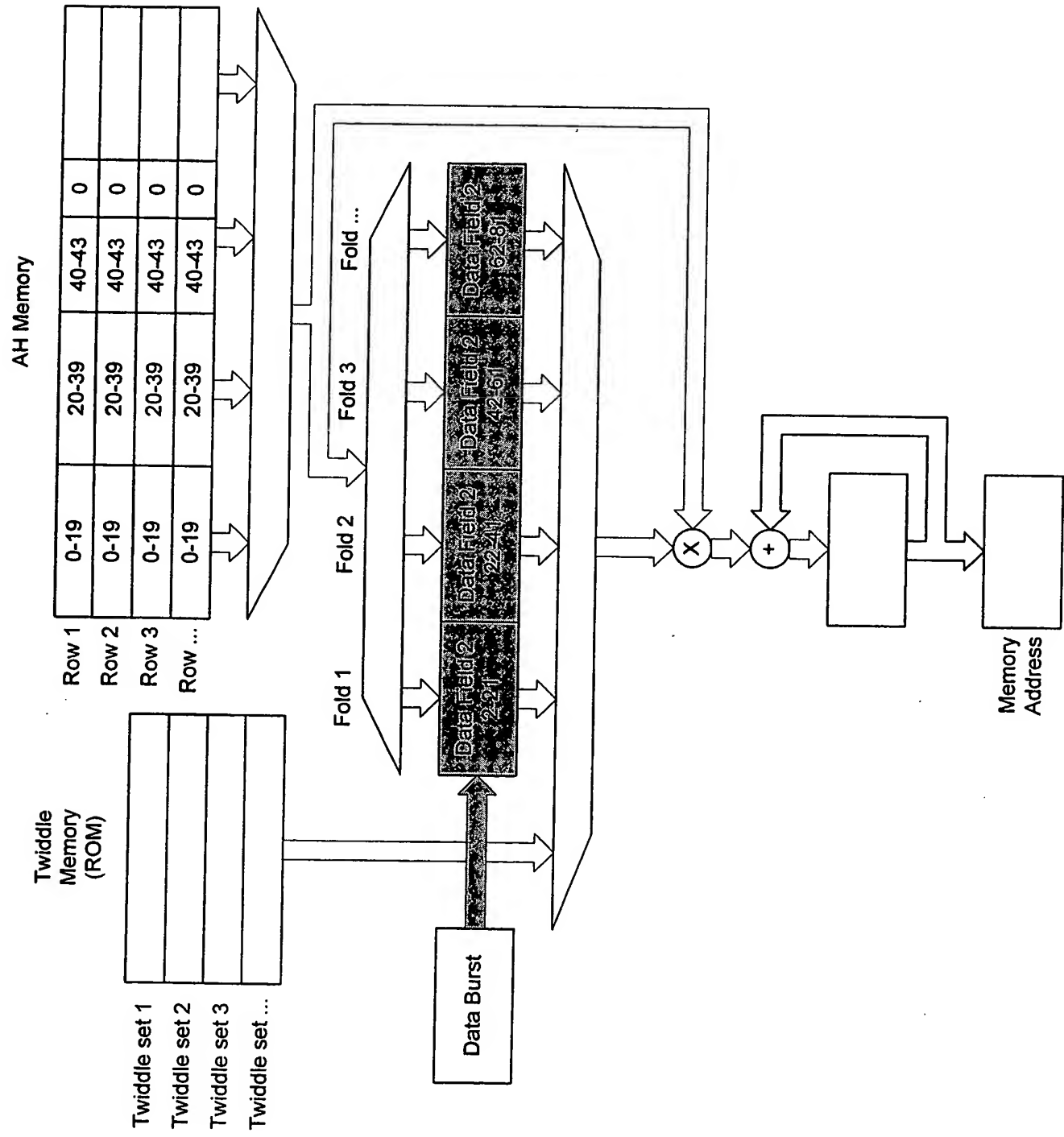


FIG- 5H

AH Memory

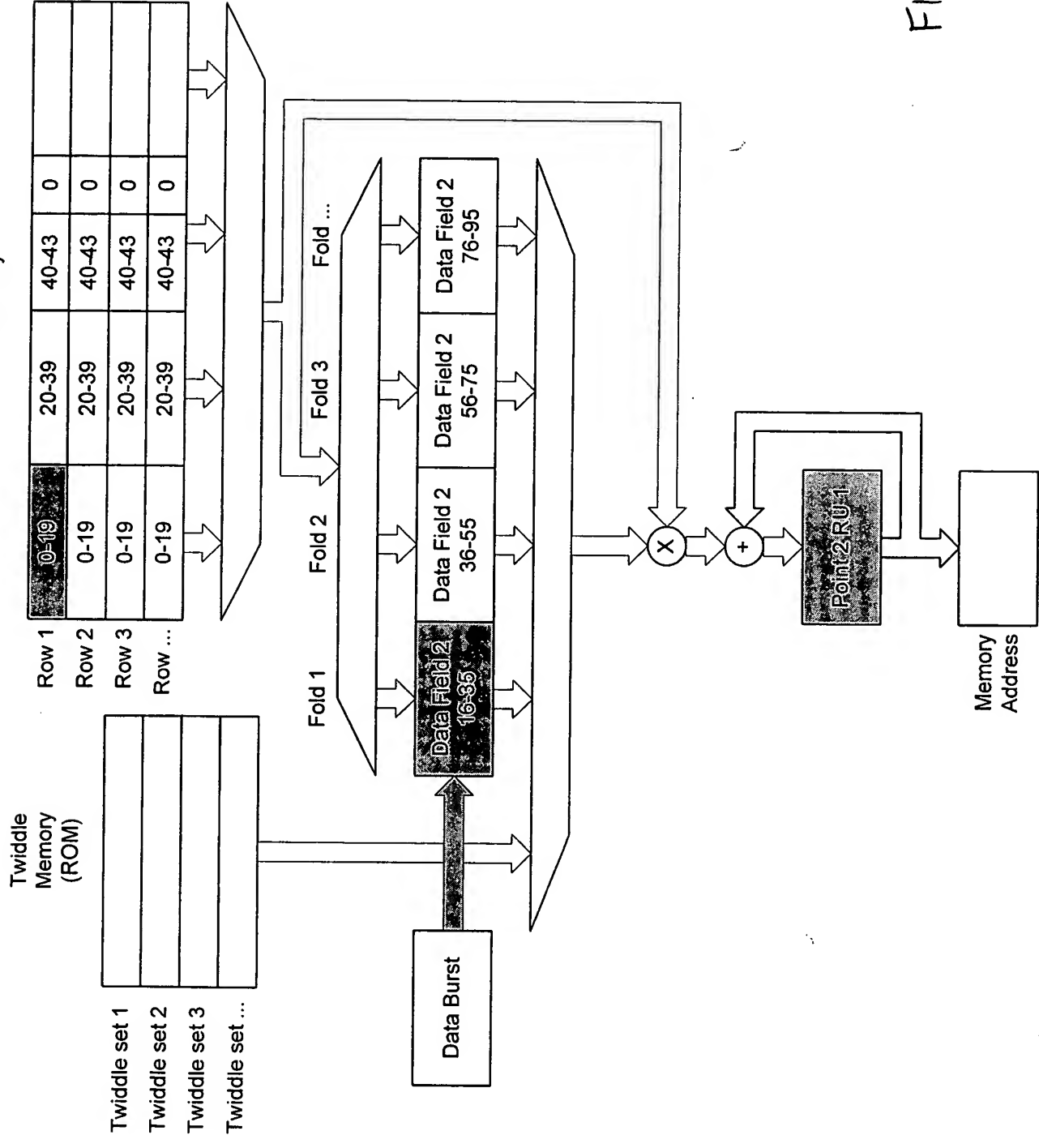
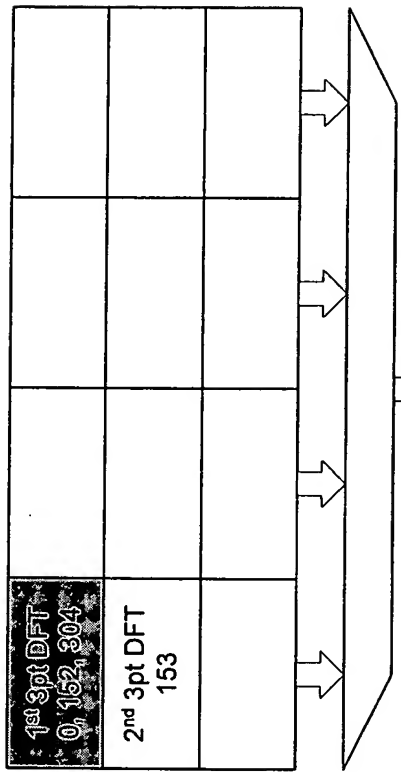
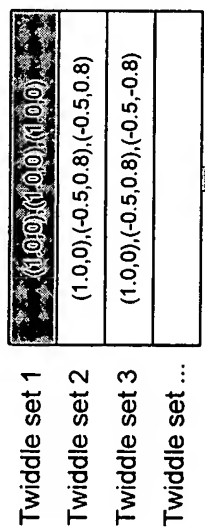


FIG 5I

External Memory



Twiddle Memory (ROM)



Data Burst

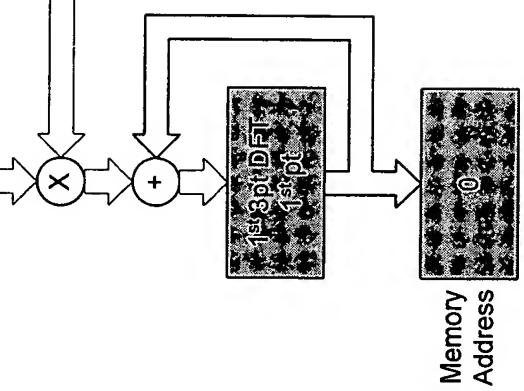
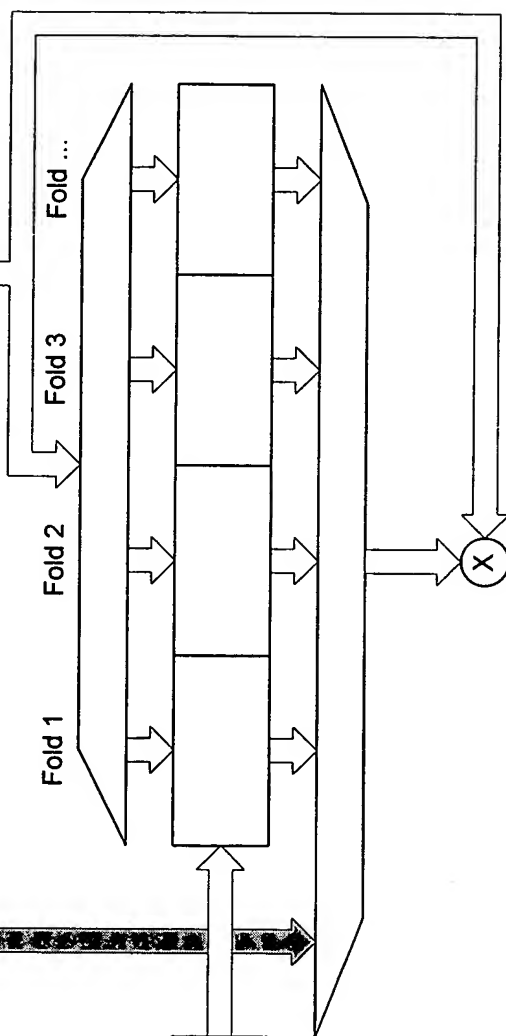
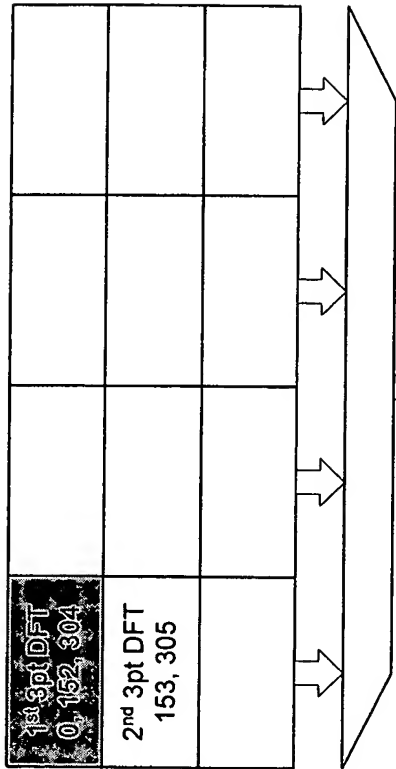


FIG 6A

External Memory



Twiddle Memory (ROM)

Twiddle set 1	(1.0,0),(1.0,0),(1.0,0)
Twiddle set 2	(1.0,0),(-0.5,0.8),(-0.5,0.8)
Twiddle set 3	(1.0,0),(-0.5,0.8),(-0.5,-0.8)
Twiddle set ...	

Data Burst

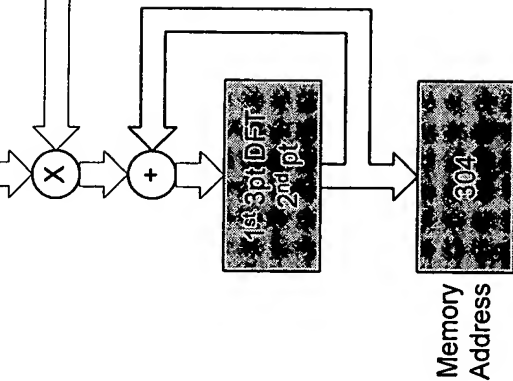
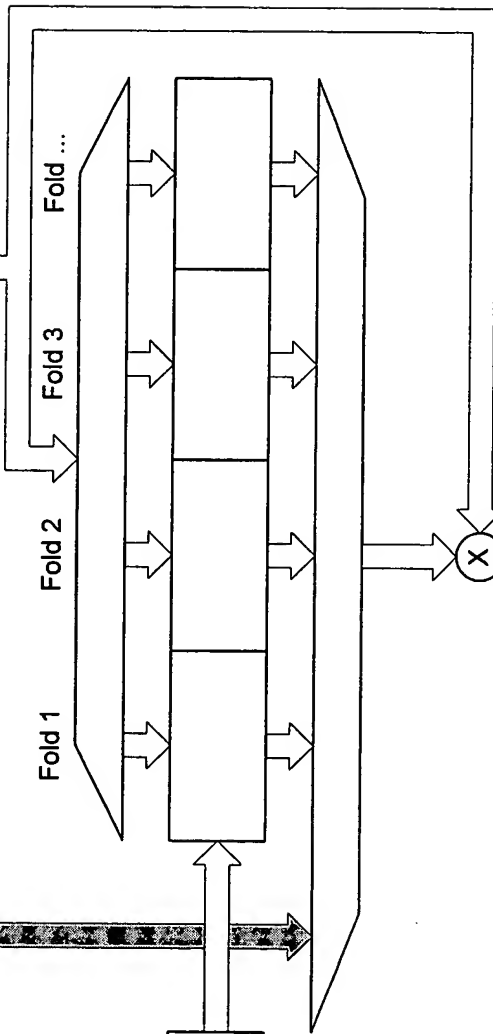
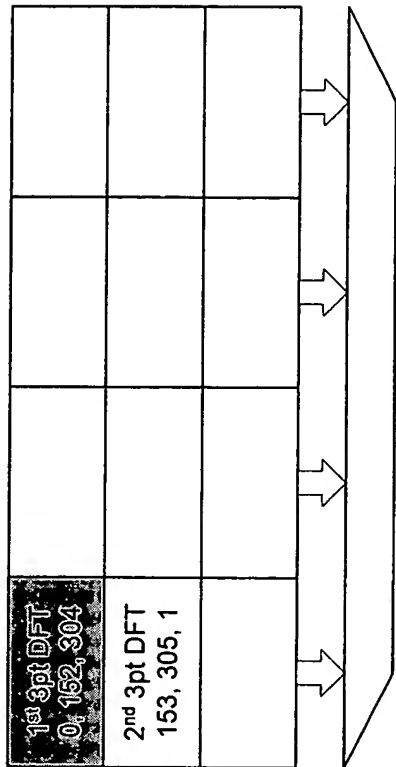


FIG- 6B

External Memory



Twiddle
Memory
(ROM)

Twiddle set 1	(1.0,0),(1.0,0),(1.0,0)
Twiddle set 2	(1.0,0),(-0.5,0.8),(-0.5,0.8)
Twiddle set 3	(1.0,0),(-0.5,0.8),(-0.5,0.8)
Twiddle set ...	

Data Burst

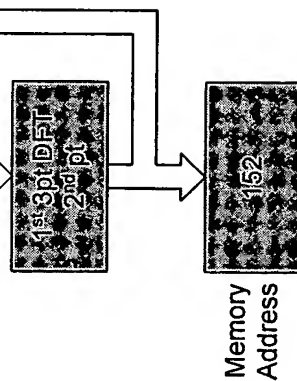
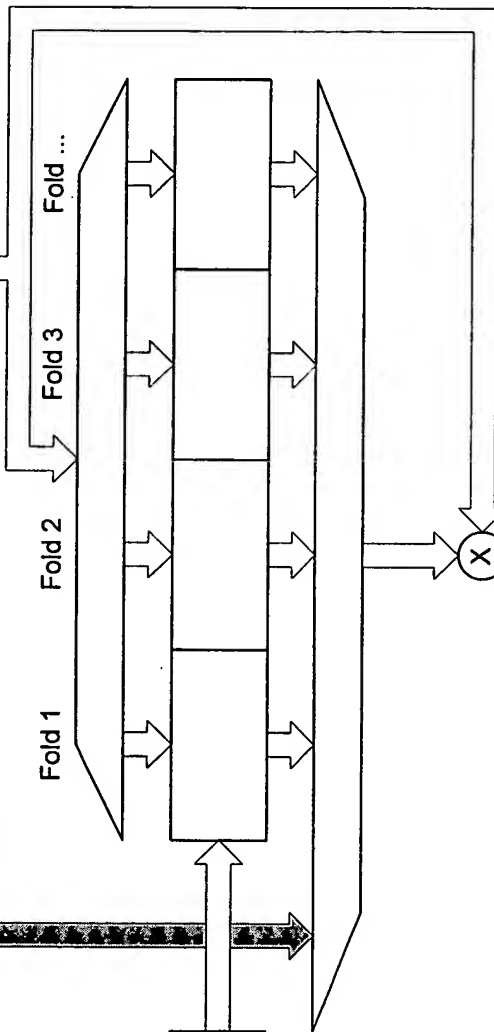


FIG 6C

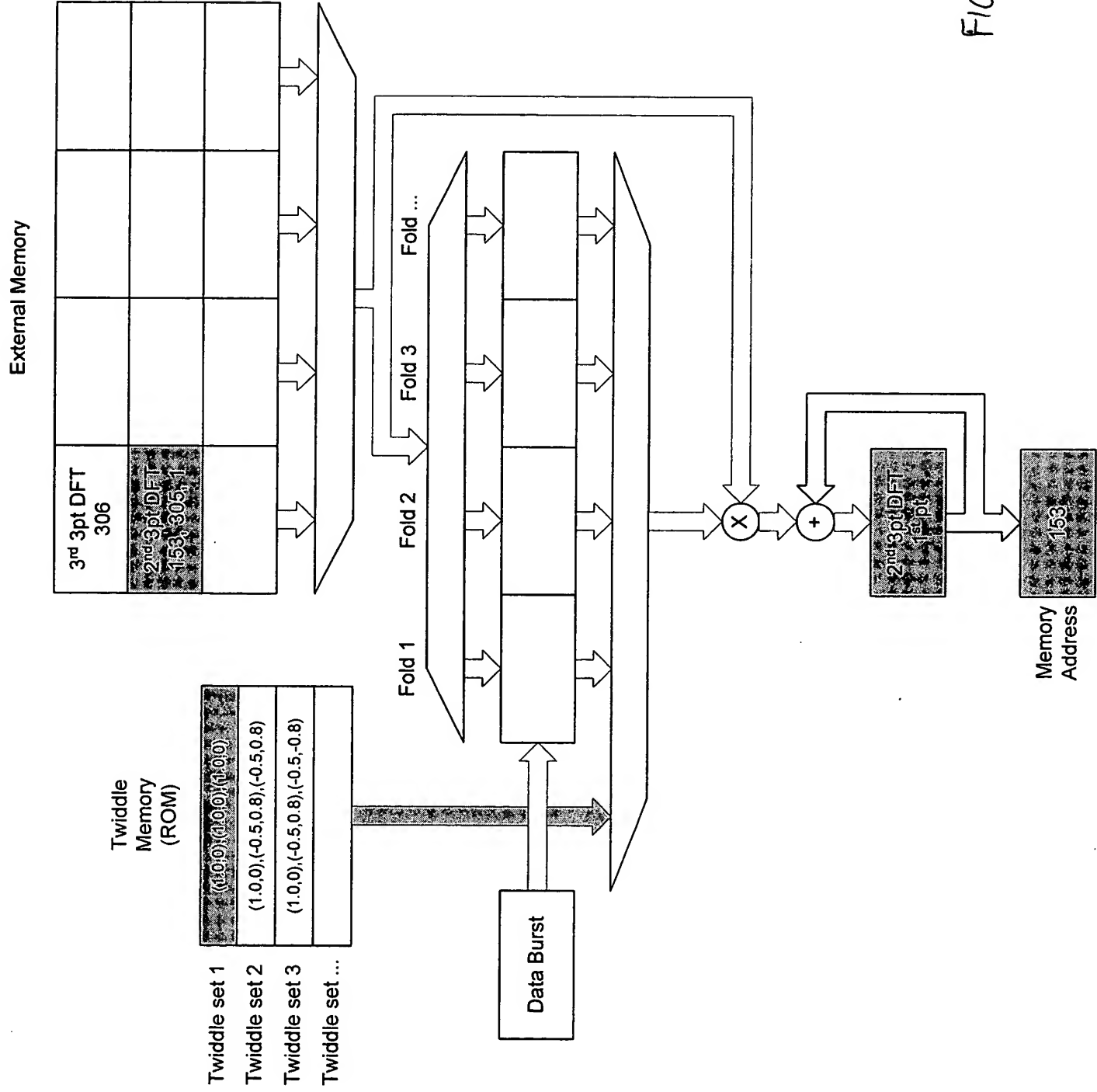
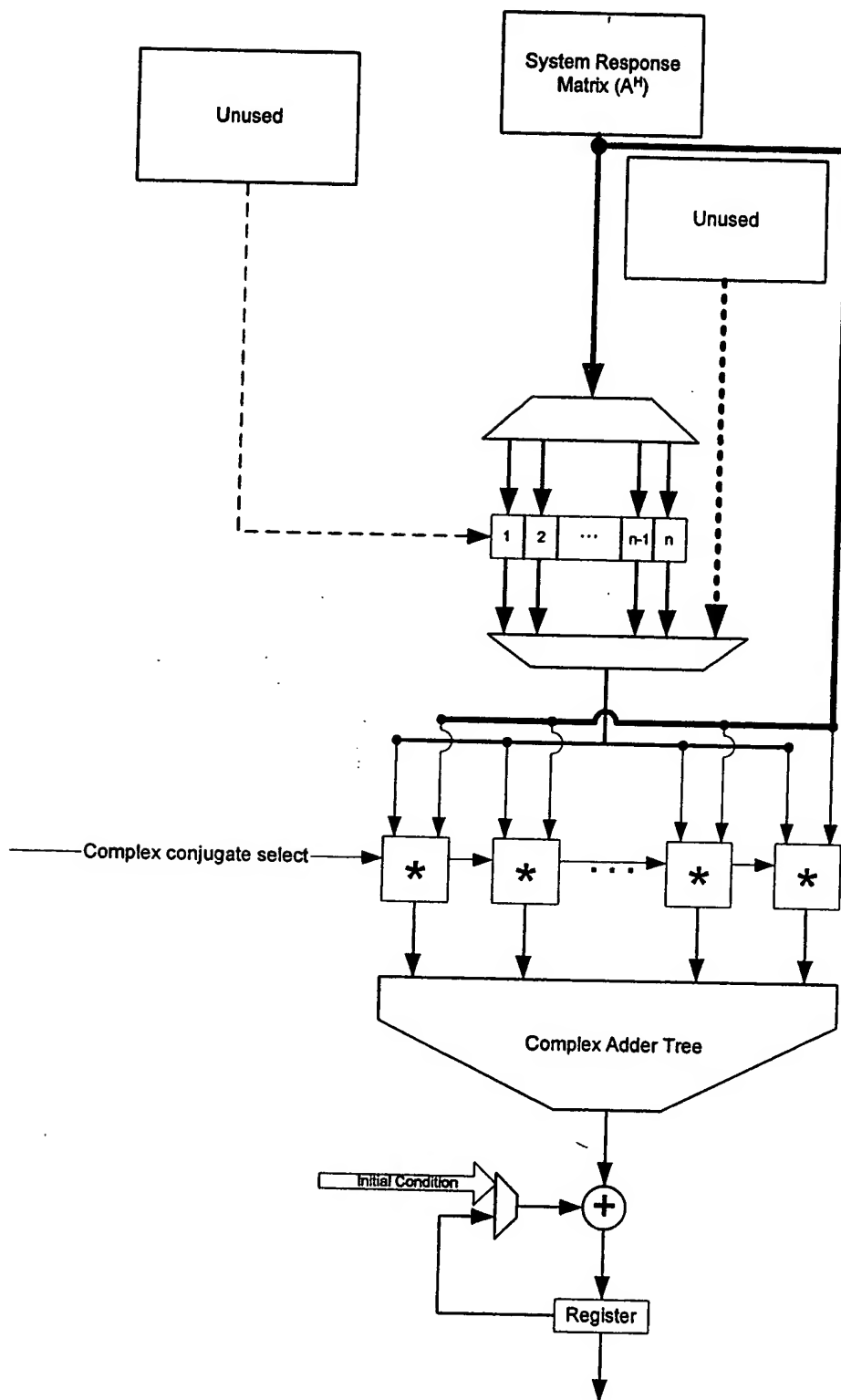
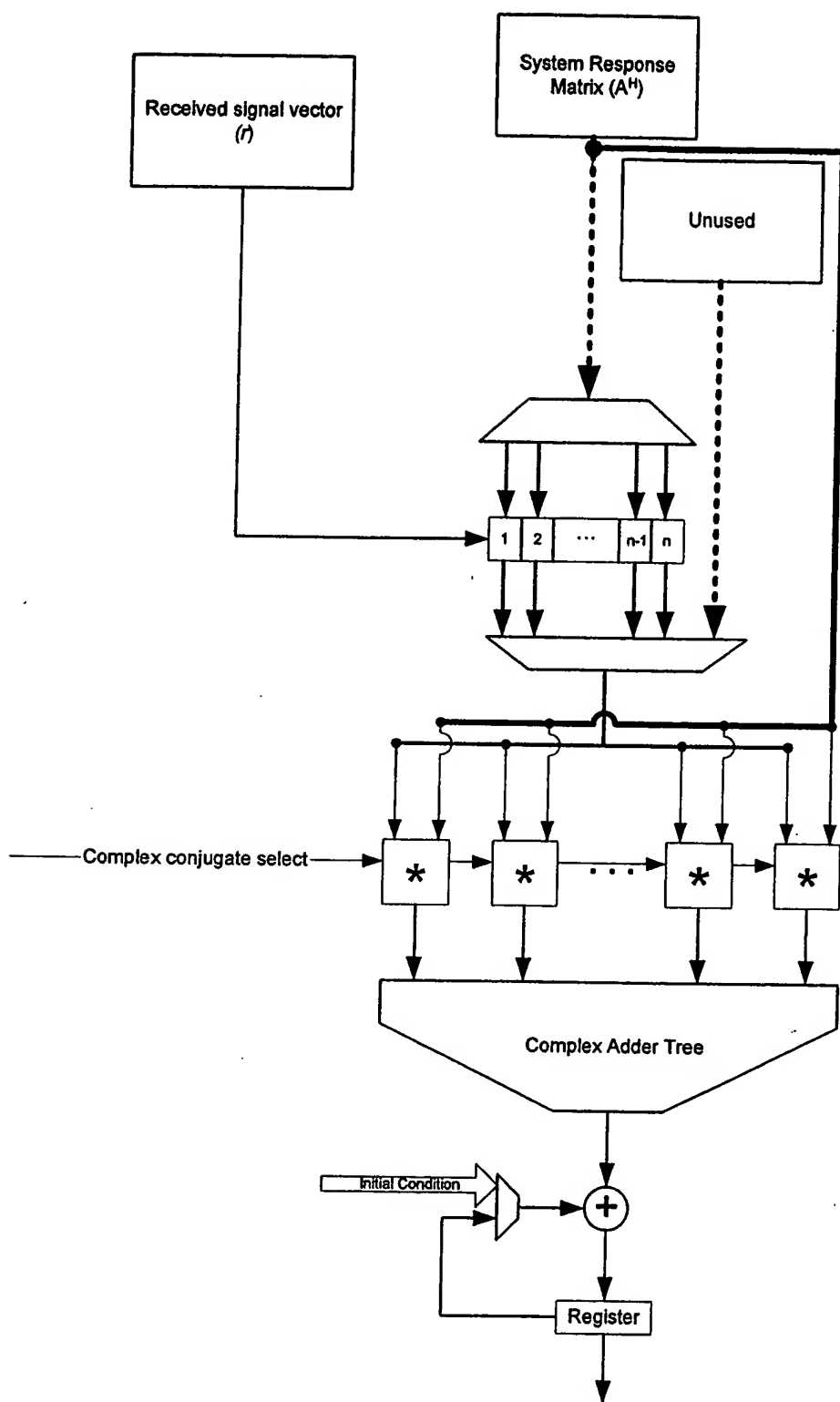


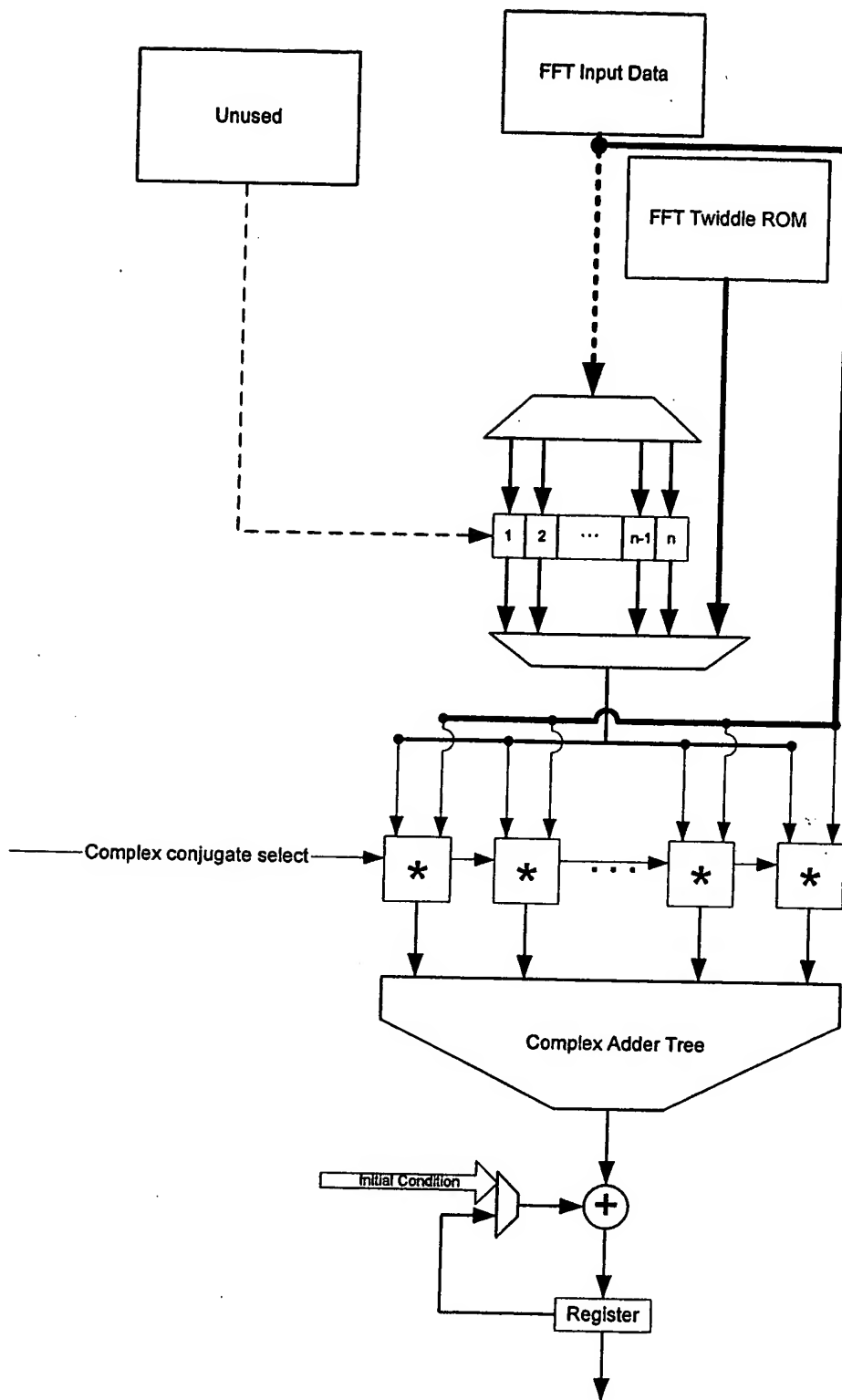
FIG 6A



7A



7B



7c